

isc Silicon NPN Power Transistor

2SD950

DESCRIPTION

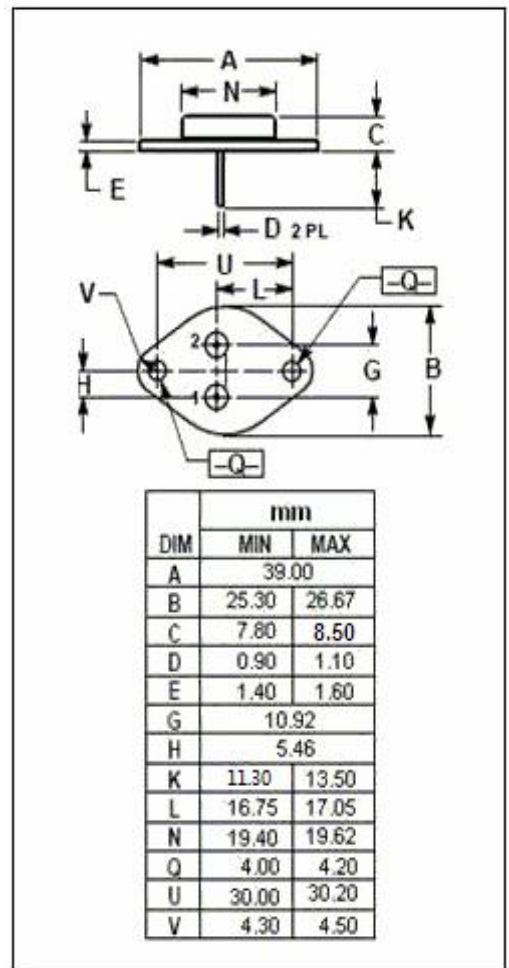
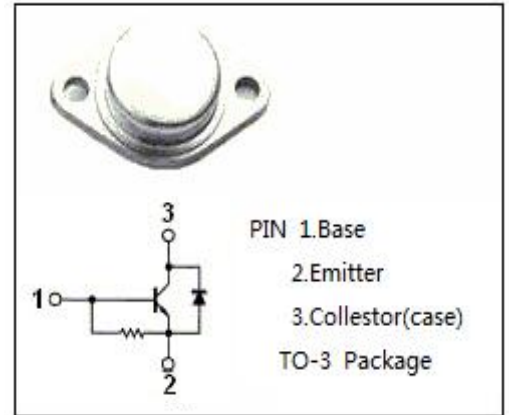
- High Breakdown Voltage-
: $V_{CBO} = 1500V$ (Min)
- Low Collector Saturation Voltage-
: $V_{CE(sat)} = 5.0V$ (Max.) @ $I_C = 2A$
- Built-in Damper Diode
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for line-operated horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CES}	Collector-Emitter Voltage	1500	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current- Continuous	3	A
I_{CP}	Collector Current- Peak	4.5	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	42	W
T_J	Junction Temperature	130	$^\circ C$
T_{stg}	Storage Temperature Range	-65~130	$^\circ C$



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{EBO}	Emitter-Base Breakdown Voltage	I _E = 200mA; I _C = 0	5.0			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.75A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 0.75A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 750V; I _E = 0			50	μA
		V _{CB} = 1500V; I _E = 0			1	mA
h _{FE}	DC Current Gain	I _C = 2A; V _{CE} = 10V	3		8	
V _{ECF}	C-E Diode Forward Voltage	I _F = 4A			1.7	V
t _{stg}	Storage Time	I _C = 2A, I _{Bend} = 0.75A; L _B = 10 μH		11		μs
t _r	Fall Time				0.9	μs

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